

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jun Dong KIM, et al.

Serial No.: 10/067,265

Filed: February 7, 2002

For: METHOD FOR MANUFACTURING BIT LINE

Group Art Unit: 2829

Examiner: A. Sarkar

TRANSMITTAL

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment Under Rule 1.111 for filing in the above-captioned patent application.


The fee has been calculated as shown below:

	Claims Remaining After Amendment	Highest Number Previously Paid For	Present Extra	Small Entity Rate Addit. Fee	(or)	Other Than A Small Entity Rate Addit. Fee
Total	12 -	20 =	0	x 09 = \$		x 18 = \$
Indep.	02 -	03 =	0	x 42 = \$		x 84 = \$
___ First Presentation of Multiple Dependent Claims				+130 = \$		+ 260 = \$
Total Additional Fee				\$		\$

\_\_\_ A check in the amount of \$\_\_\_ is attached for:

XXXX If a Petition for Extension of Time is necessary and the Petition and/or the check is not enclosed, this will act as the Petition and applicant herewith petitions the Commissioner to extend the time for response and charge any fees necessary under 37 CFR 1.17 (a)(1)-(5) to Deposit Account No. 06-1358. The Commissioner is also authorized to charge payment of any other additional fees associated with this communication or credit any overpayment to Deposit Account No. 06-1358. A duplicate copy of this sheet is attached.

Respectfully submitted,  
JACOBSON HOLMAN PLLC

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Date: May 5, 2003  
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PATENT  
ATTY. DOCKET NO.: P66262US0

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**AMENDMENT UNDER RULE 1.111**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action (Paper No. 5) dated February 4, 2003, kindly  
amend the above-identified patent application as follows:

**IN THE CLAIMS**

Please cancel claims 2-4 without prejudice or disclaimer and amend claims 1, 5  
and 9-11 to read as follows:

1. (Amended) A method of manufacturing a bit line, the method comprising the steps of:

successively forming a conducting layer and an insulating layer on a substrate, the  
conducting layer serving to form a bit line;

forming a photoresist film on the insulating layer;

etching the photoresist film using a photolithography process so as to form a first  
mask pattern on the insulating layer in such a manner that a desired region of the insulating layer  
is exposed;

#6A Aut  
M. Brunson  
5/7/03  
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